

FIG. 1 shows a perspective view of a multi-layered electronic device assembly. The assembly includes a top layer (6) and a bottom layer (16). Between them are several intermediate layers or components labeled 4a, 4b, 4c, 4d, and 4e. A central component (13) is visible, surrounded by other elements like 14, 15, and 16. There are also labels 35, 36, 56, and 57. Section lines I-I and II-II are indicated with arrows.

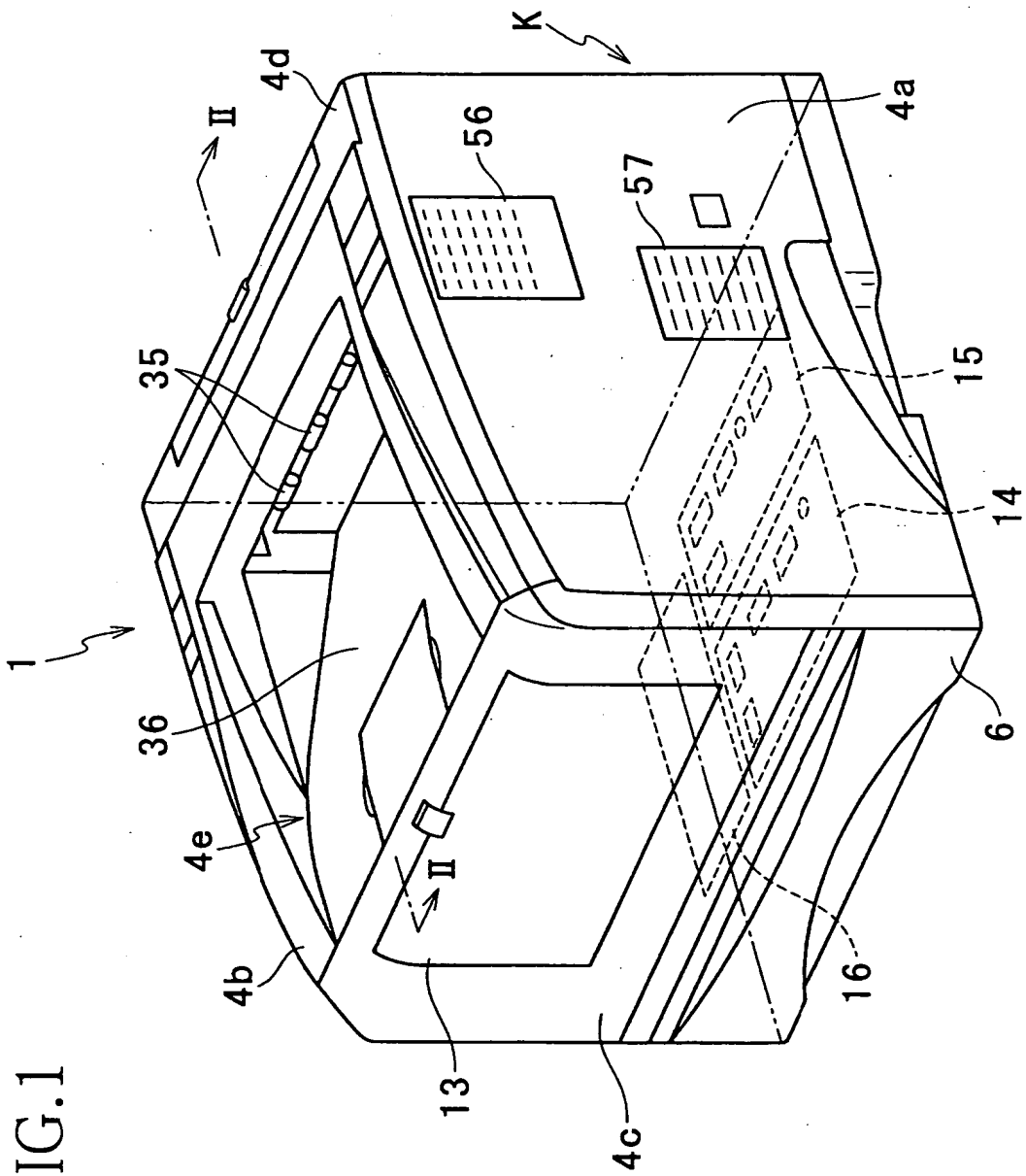


FIG. 2

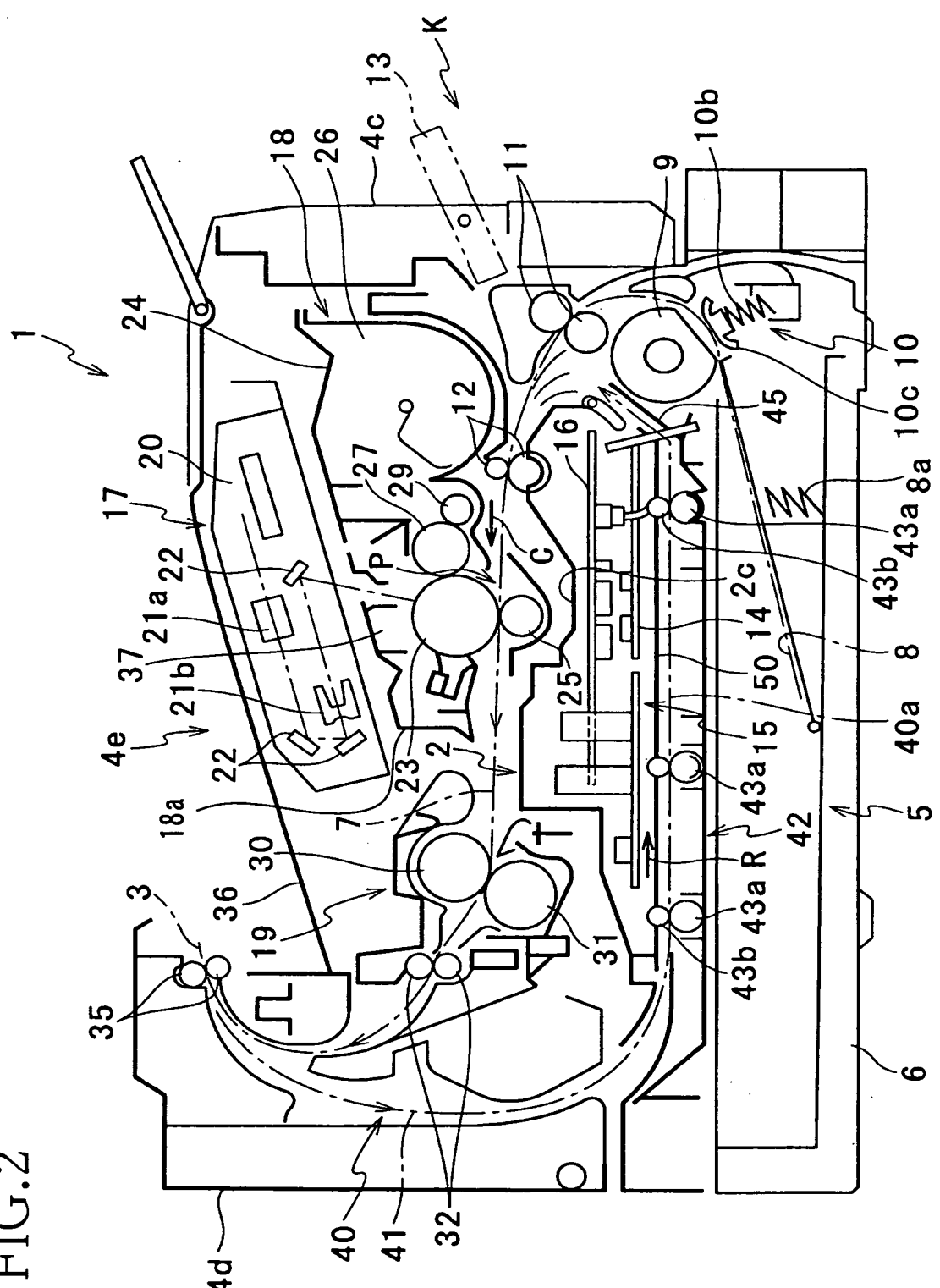


FIG.3

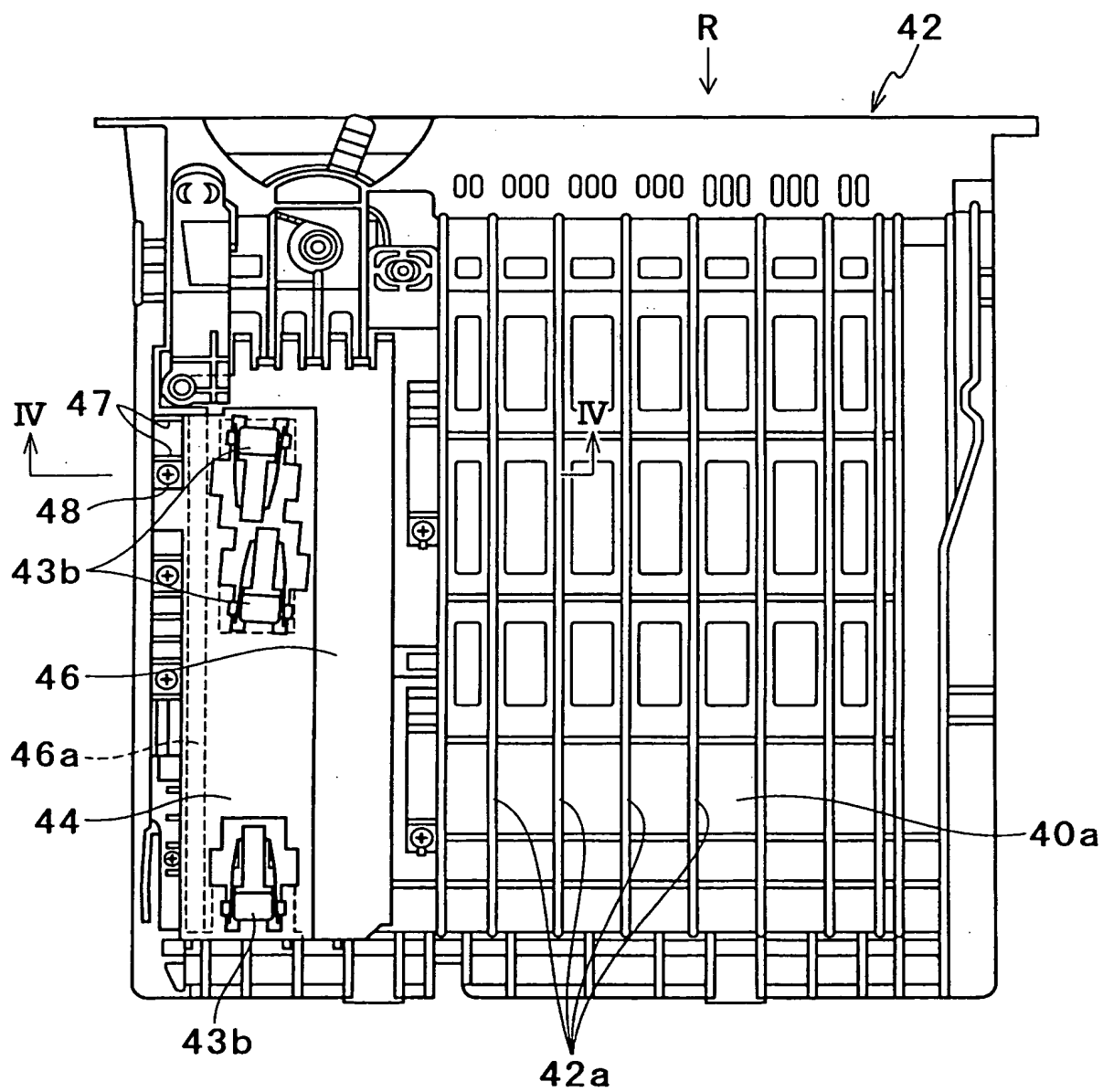


FIG. 4

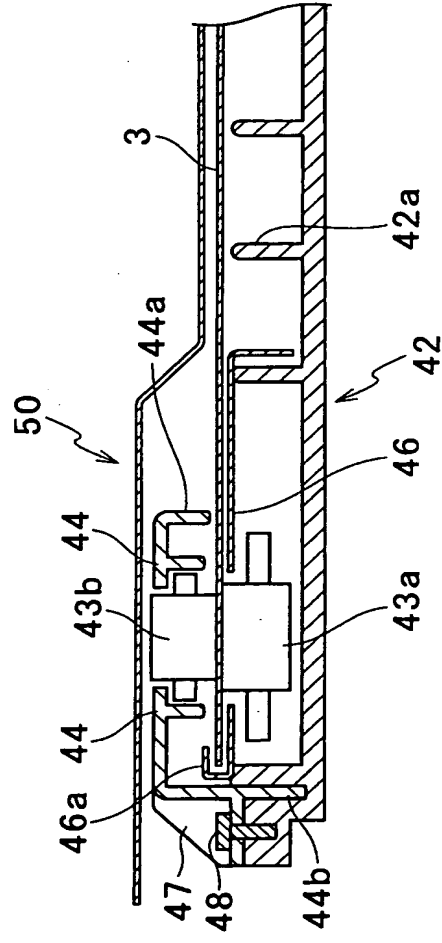


FIG.5

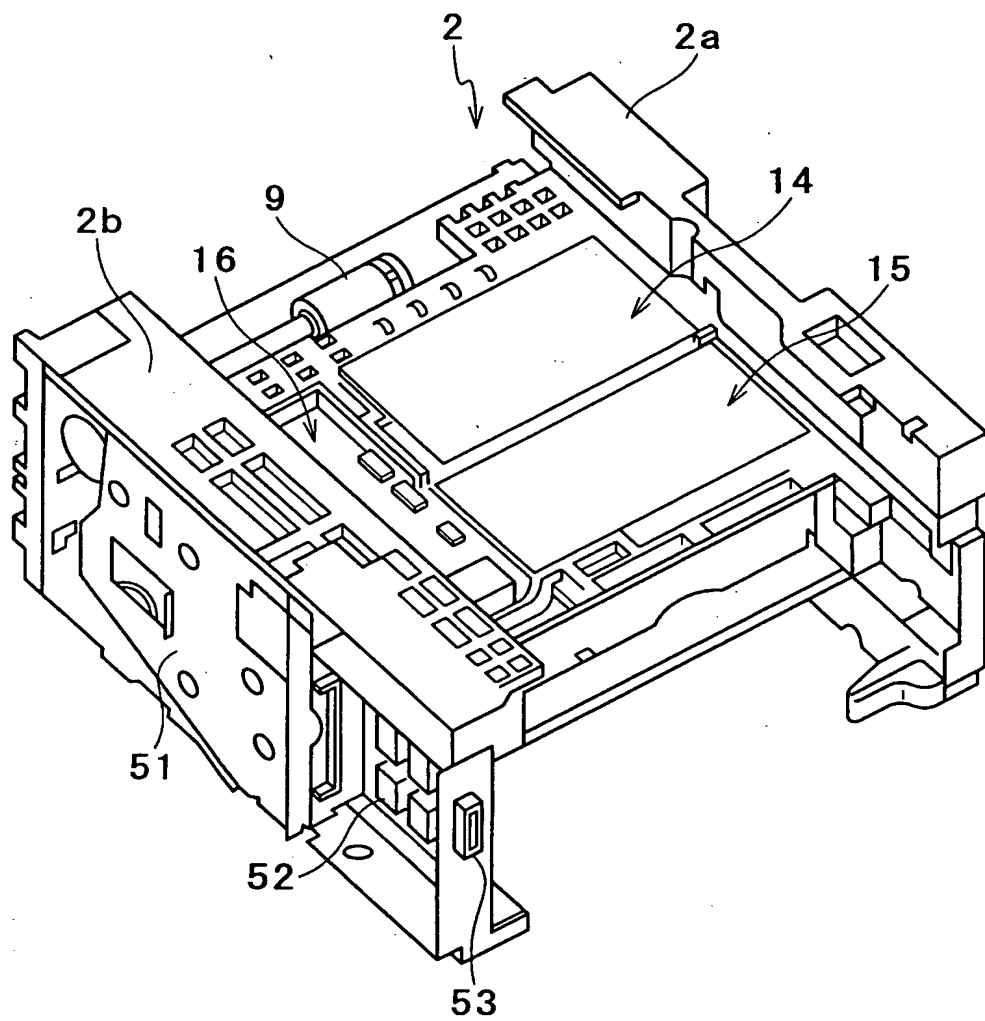


FIG.6

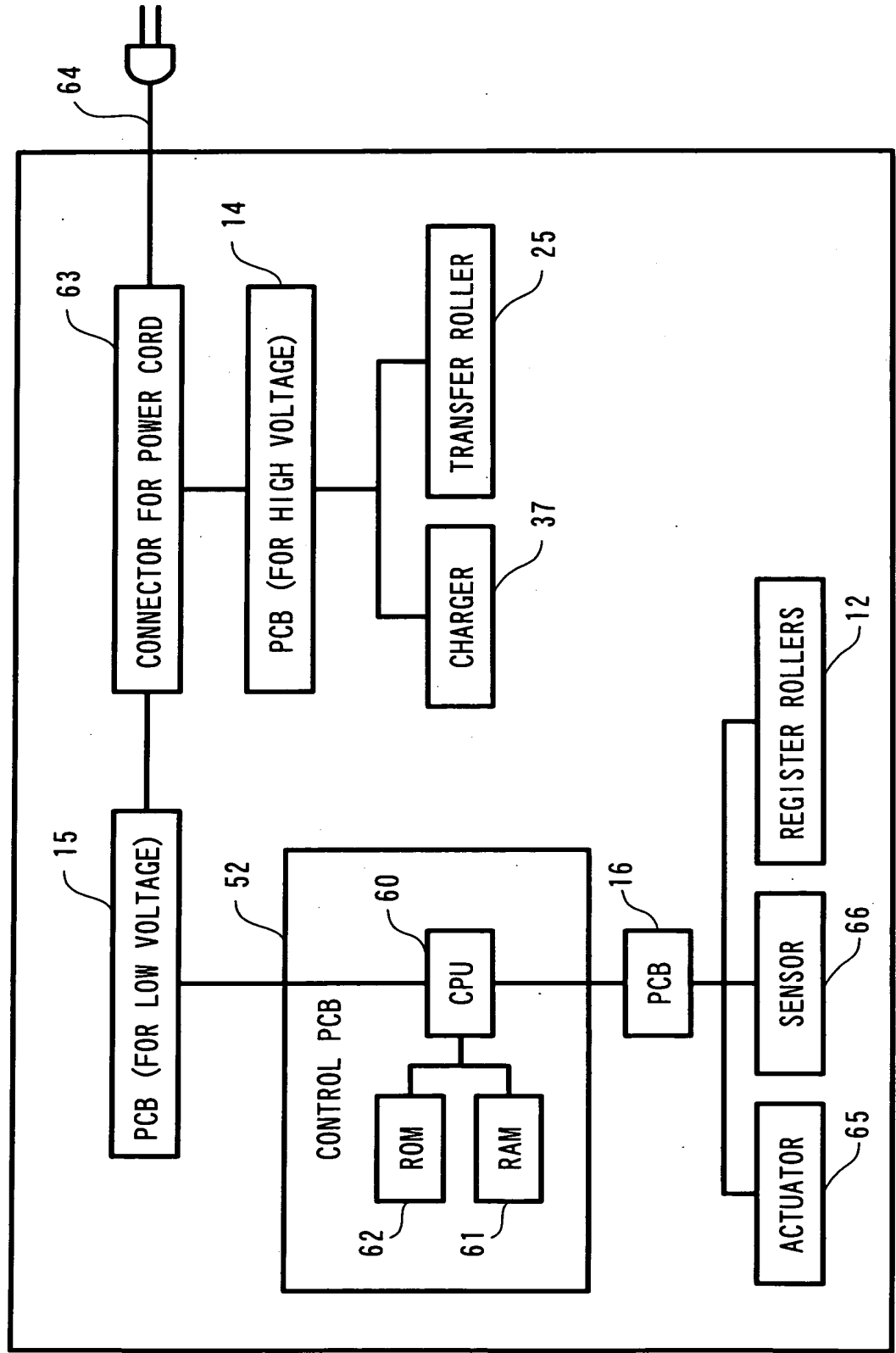


FIG.7

